

To Overlay or to Customize?

Revisiting Architectural Choices in Heterogeneous Systems

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Brown University; Wayne State University*

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<https://peipeizhou-eecs.github.io/>



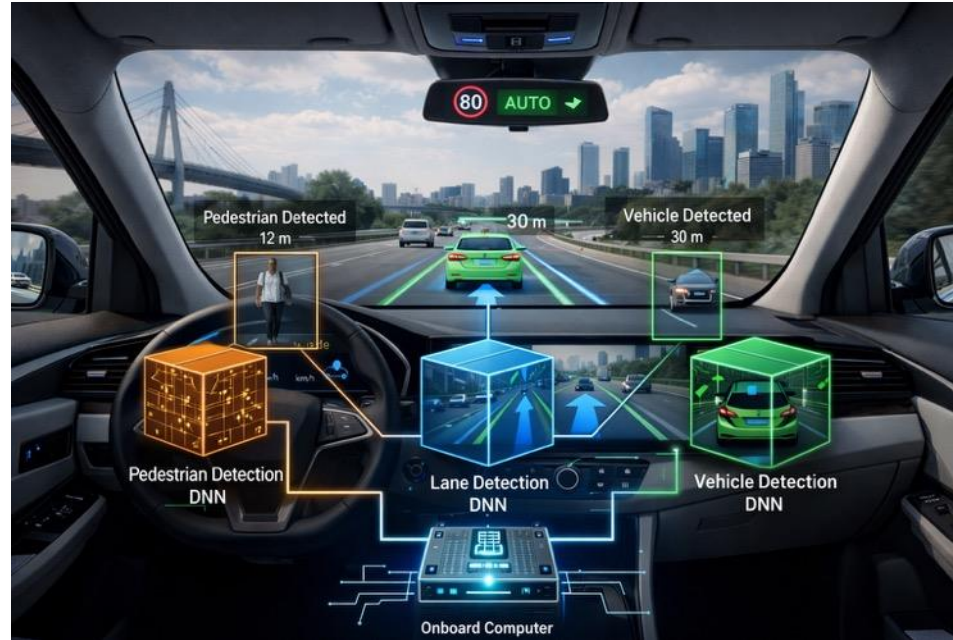
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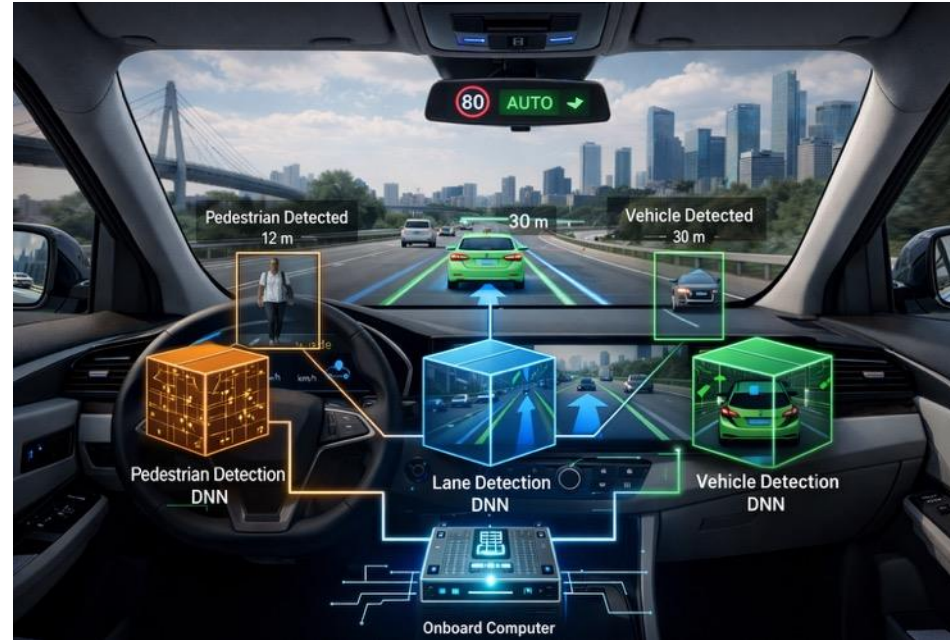
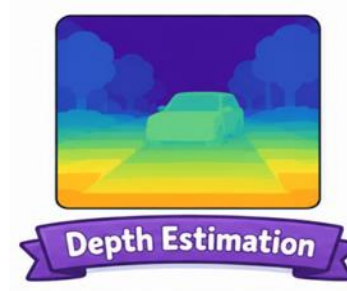


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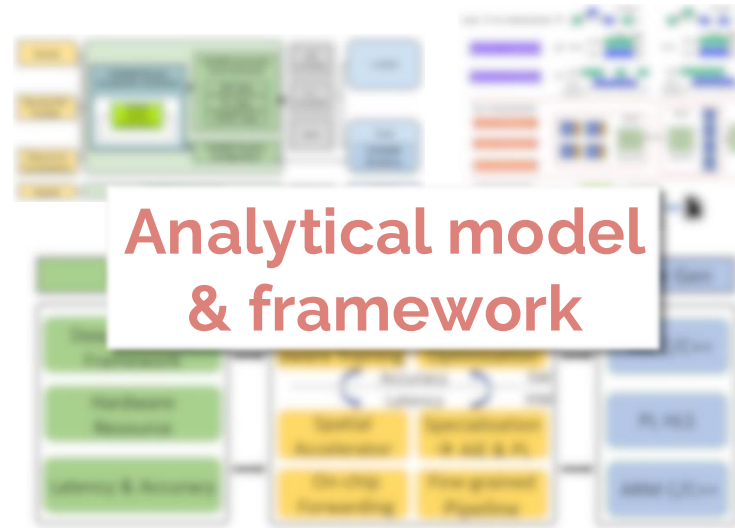
Tasks behind modern autonomous driving systems.

Design Choices

- Customization accelerators:

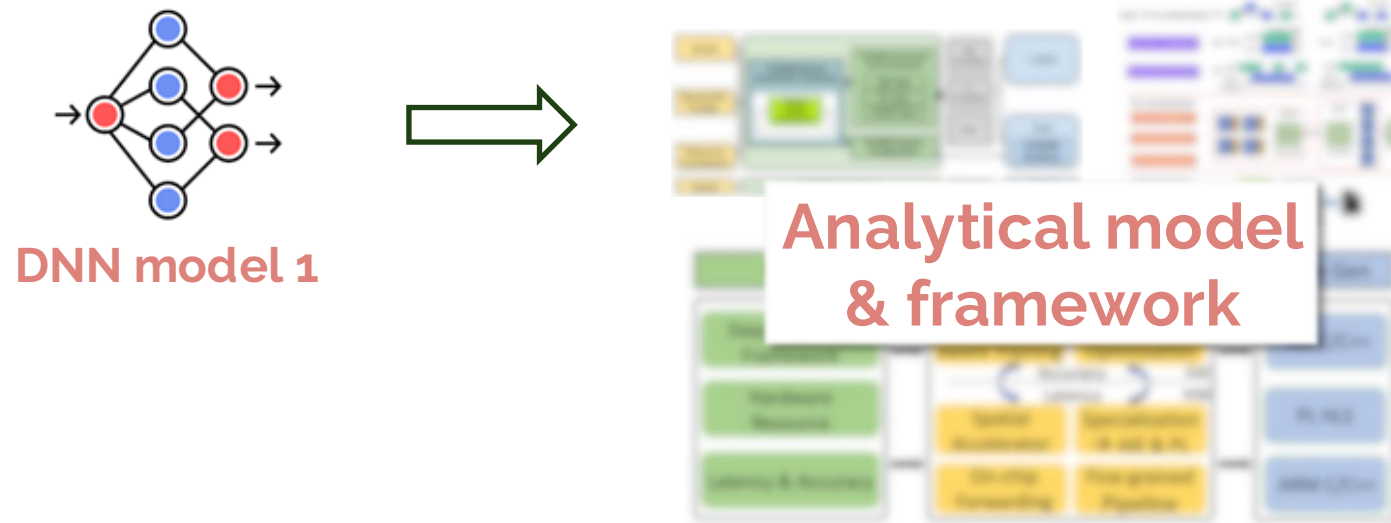
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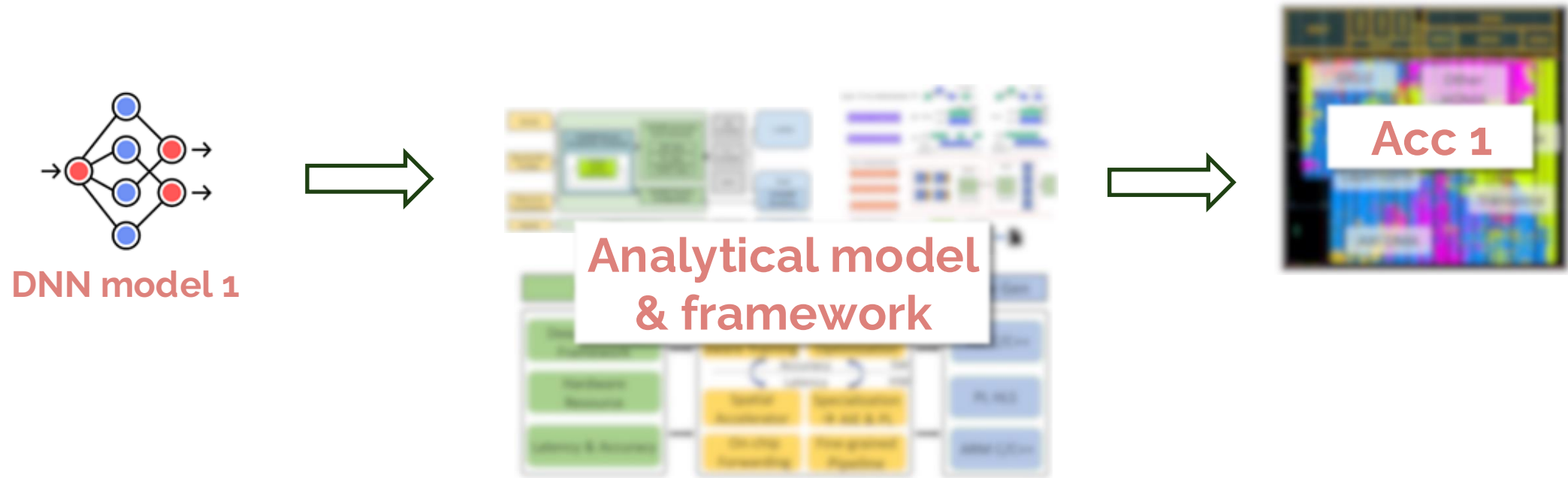
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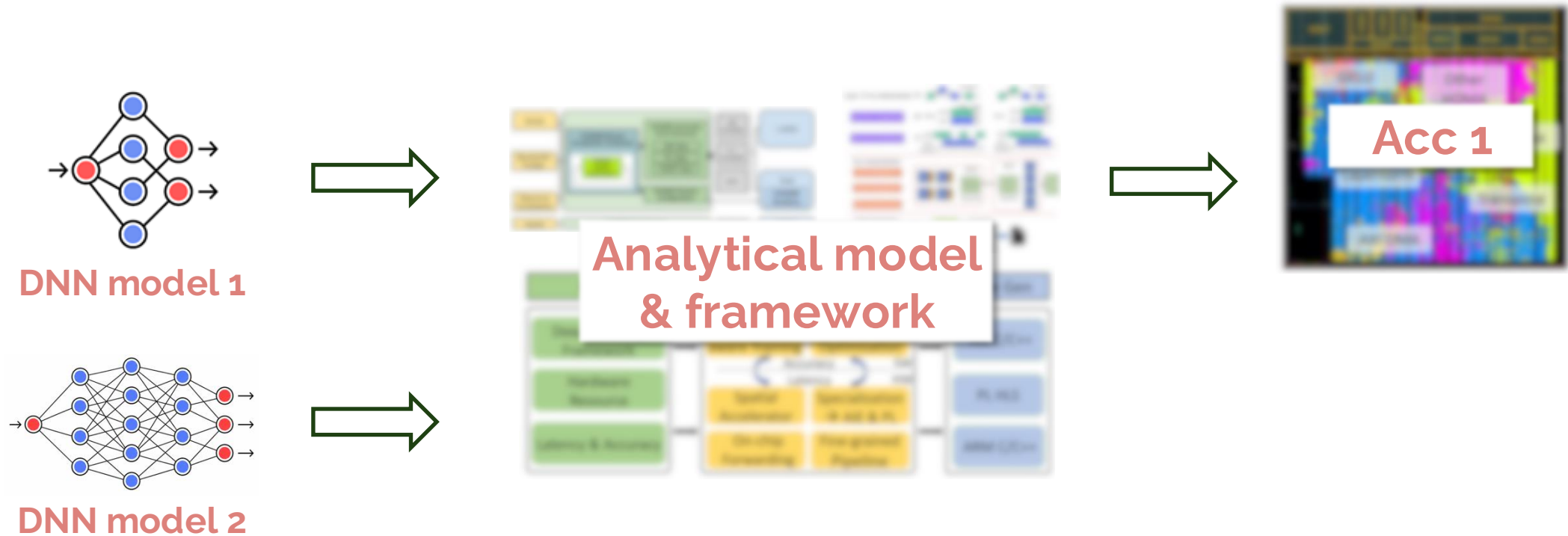
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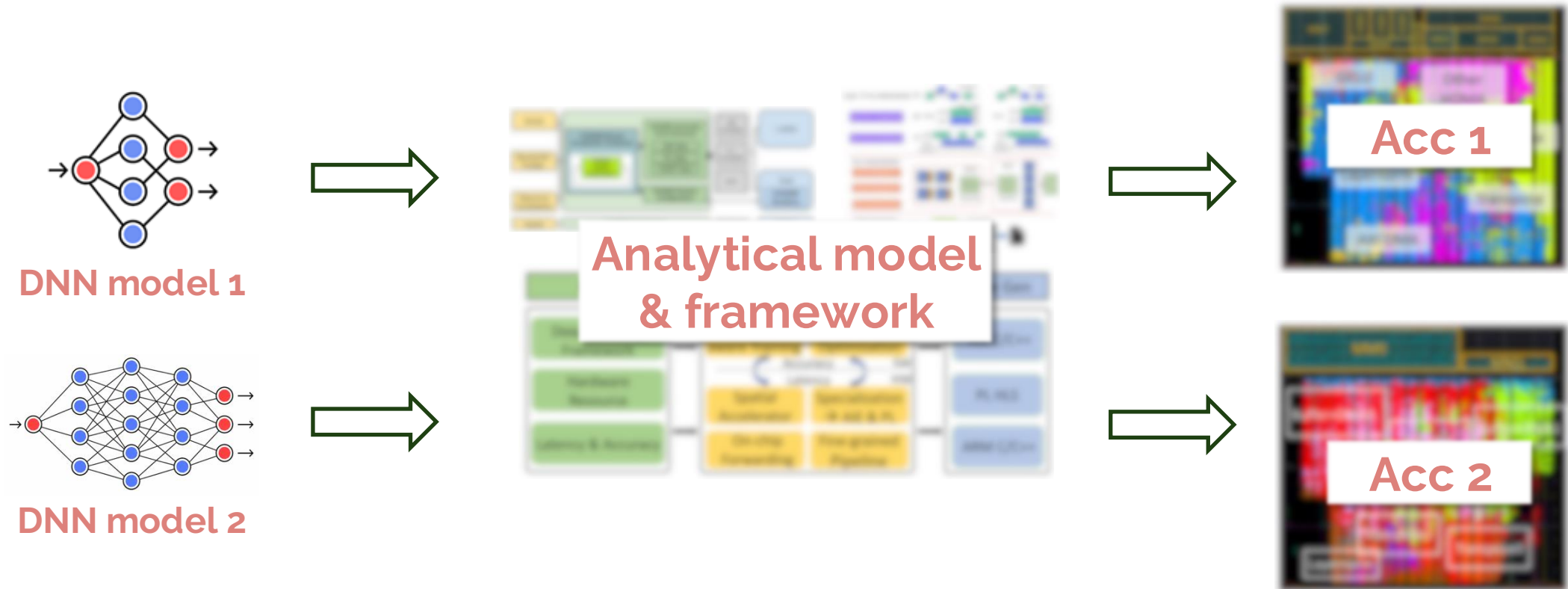
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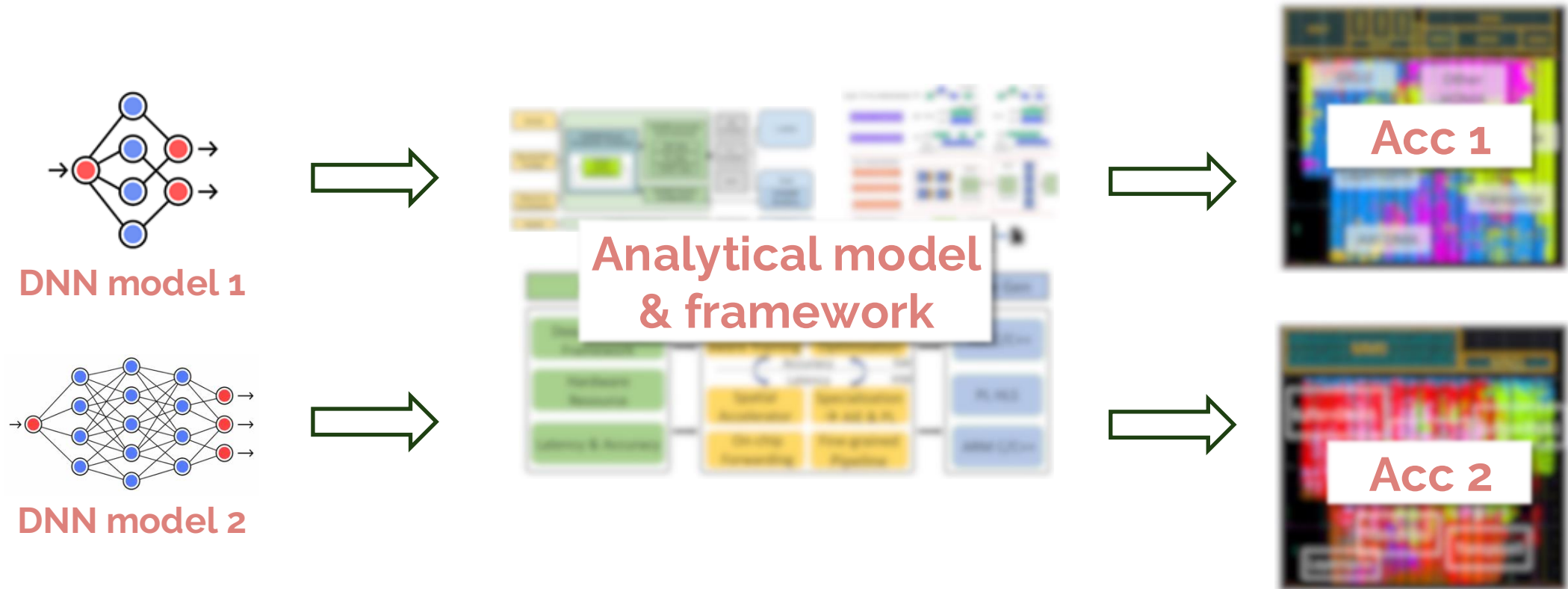
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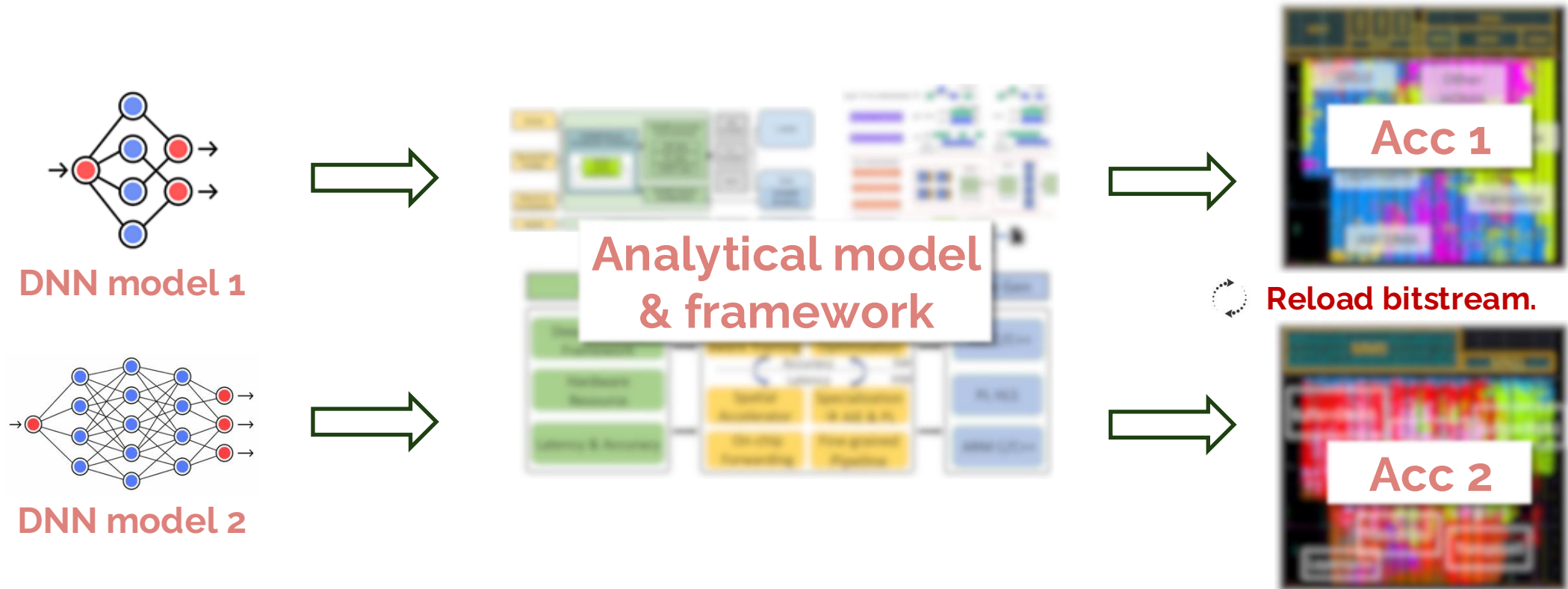
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- **Dedicated architectures** are instantiated for **different applications**.

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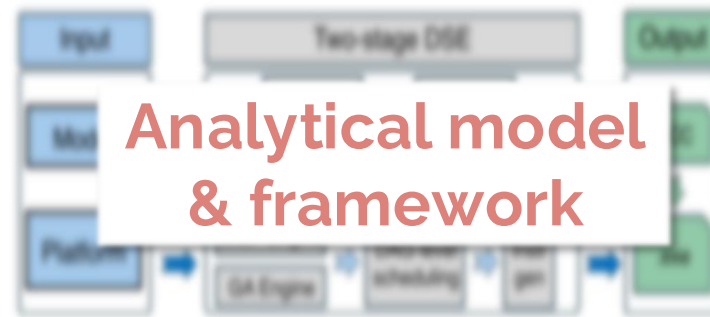
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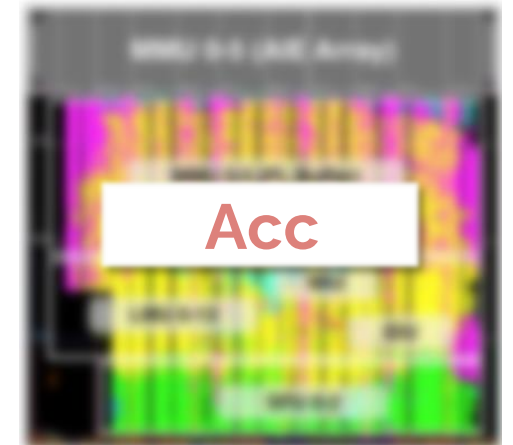
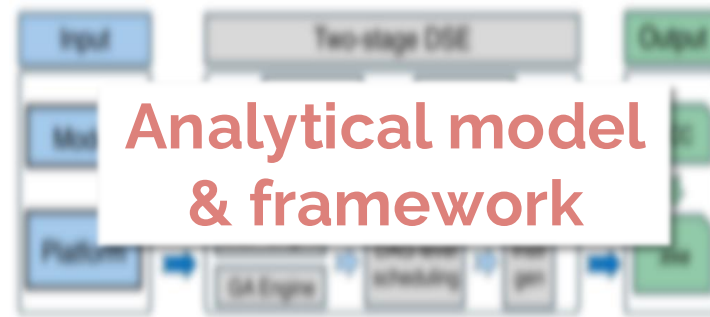
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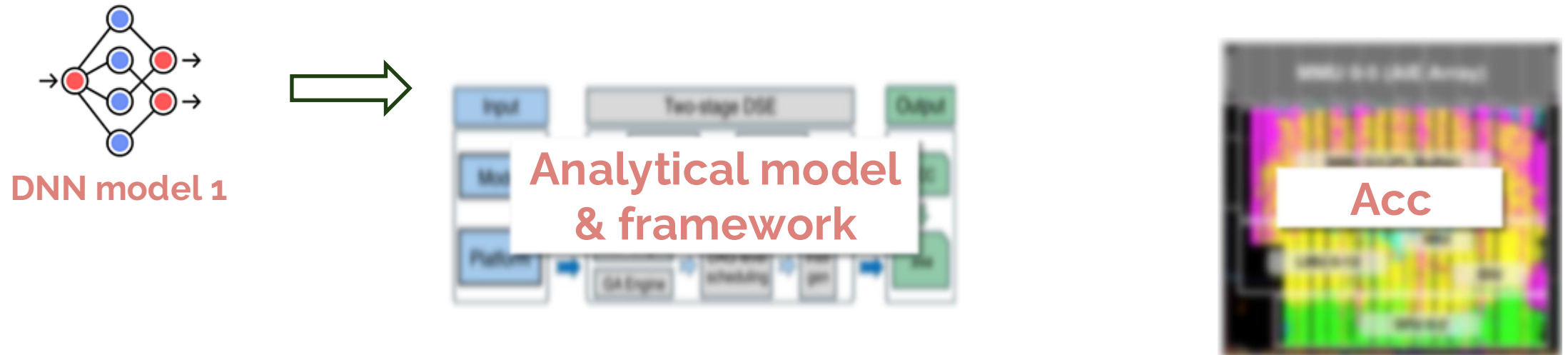
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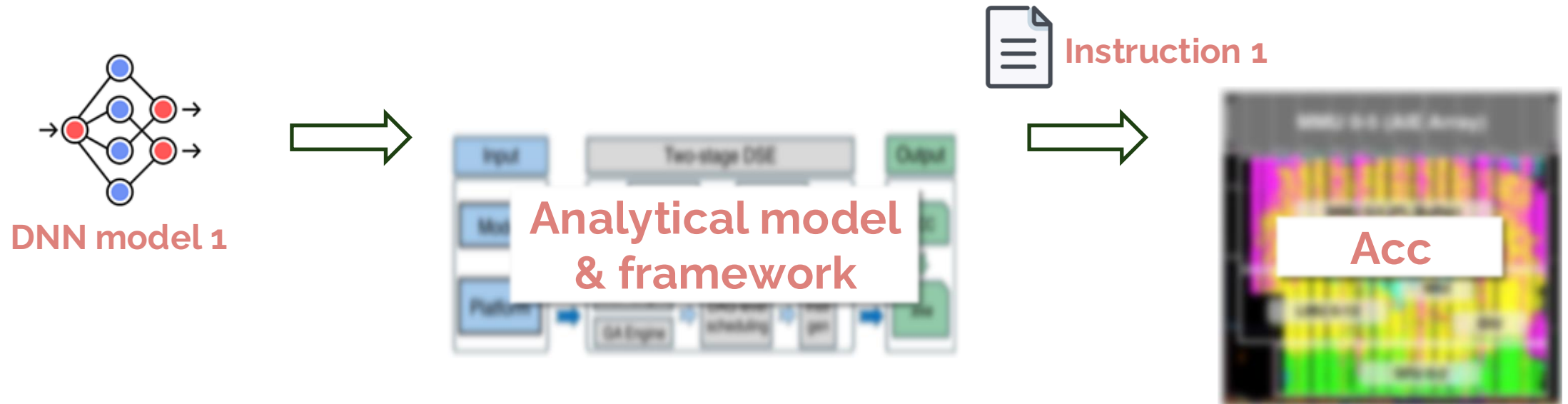
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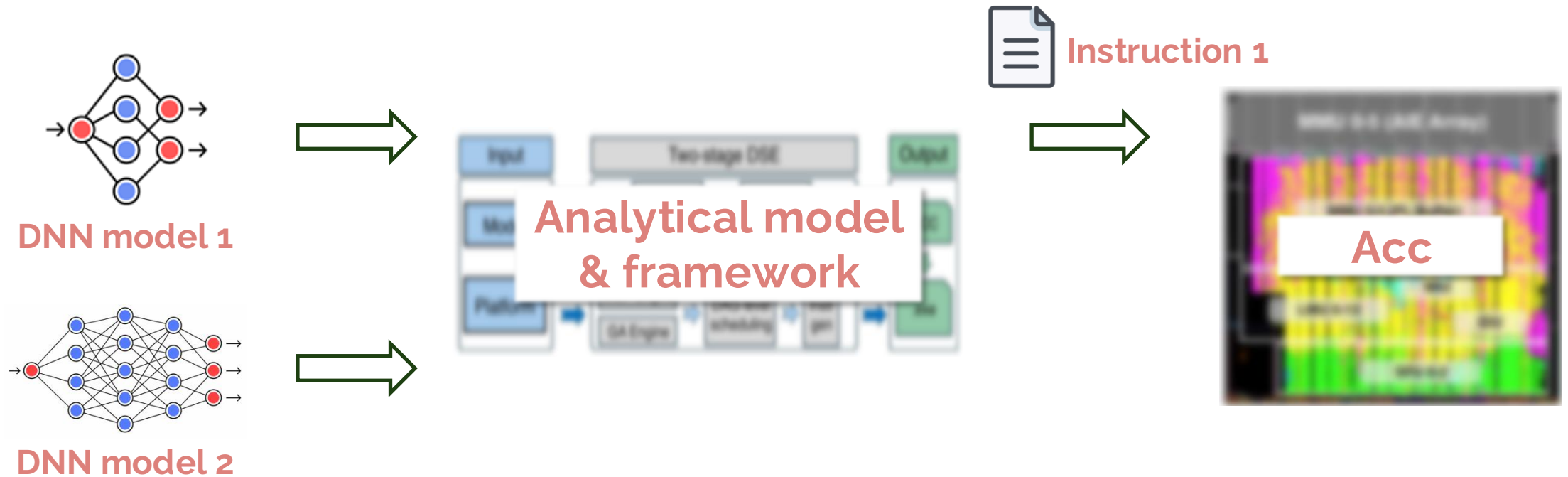
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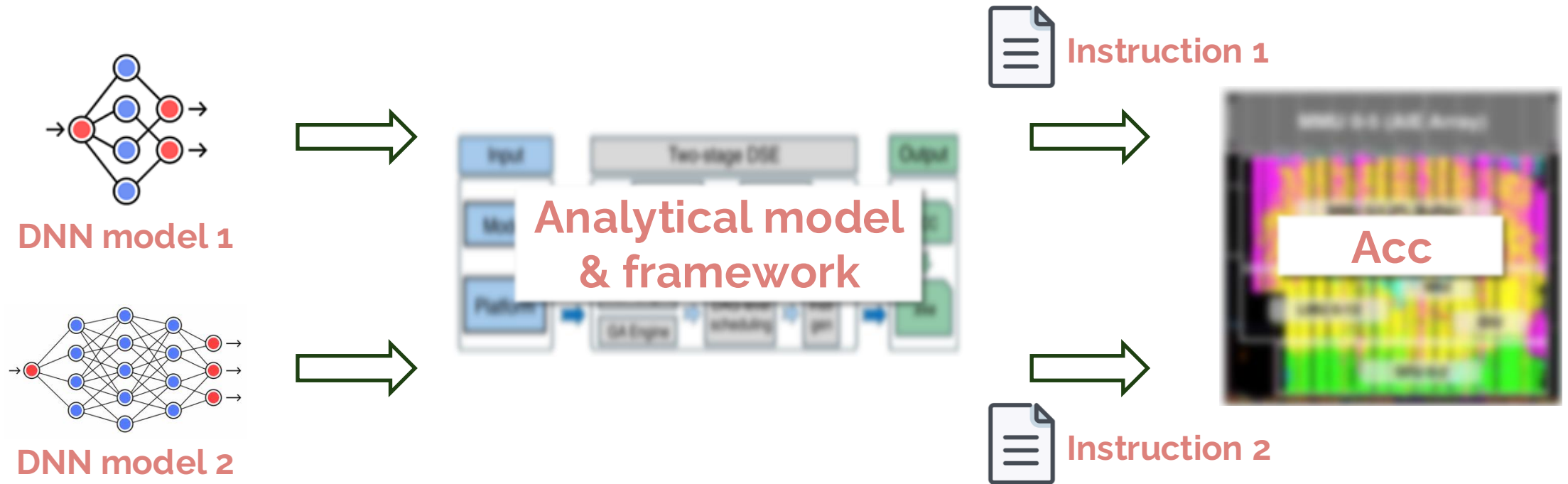
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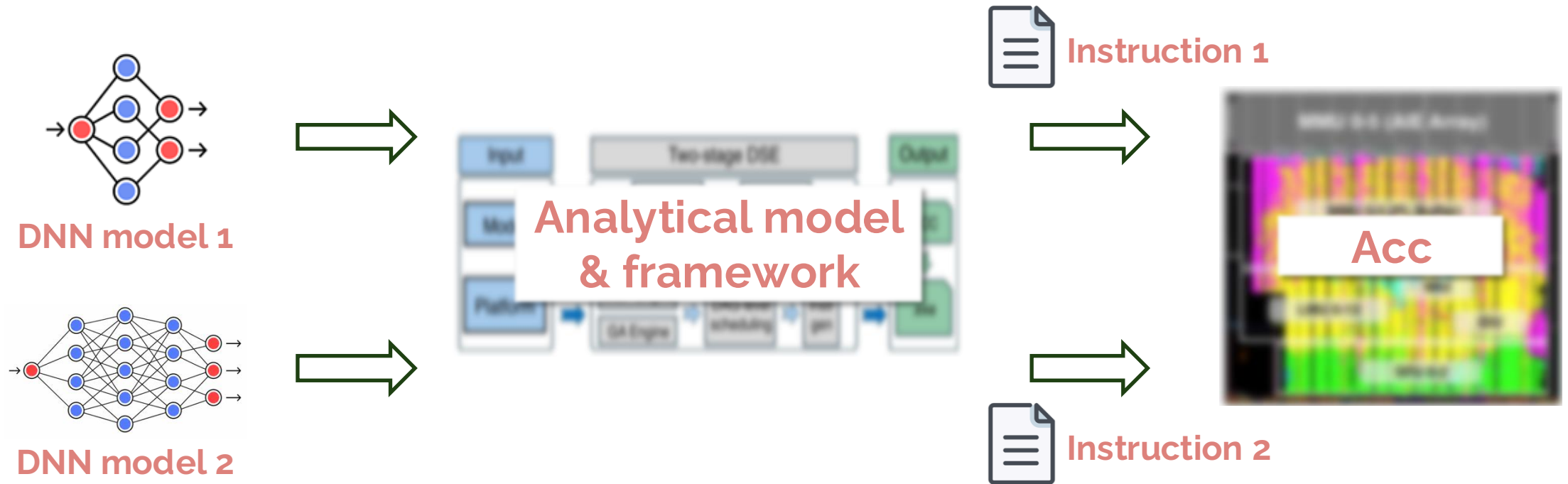
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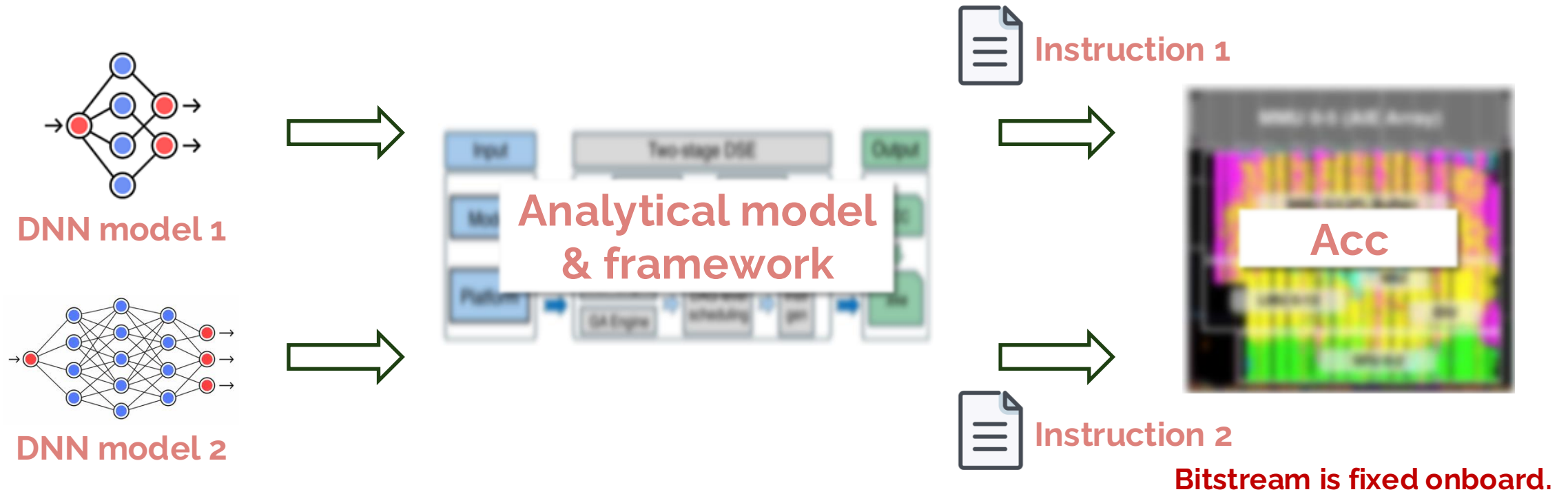
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- Different applications share the same architecture with different instructions.

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Customization accelerators



Overlay accelerators

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Customization accelerators

- Dedicated customization for specific DNN models.
- Better **efficiency** and **performance**.
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Overlay accelerators

- Multiple DNN models share the same architecture.
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- Worse **efficiency** and **performance**.

Design Choices

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Customization accelerators

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Overlay accelerators

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To overlay or to Customization?

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20Hz,
50ms/frame



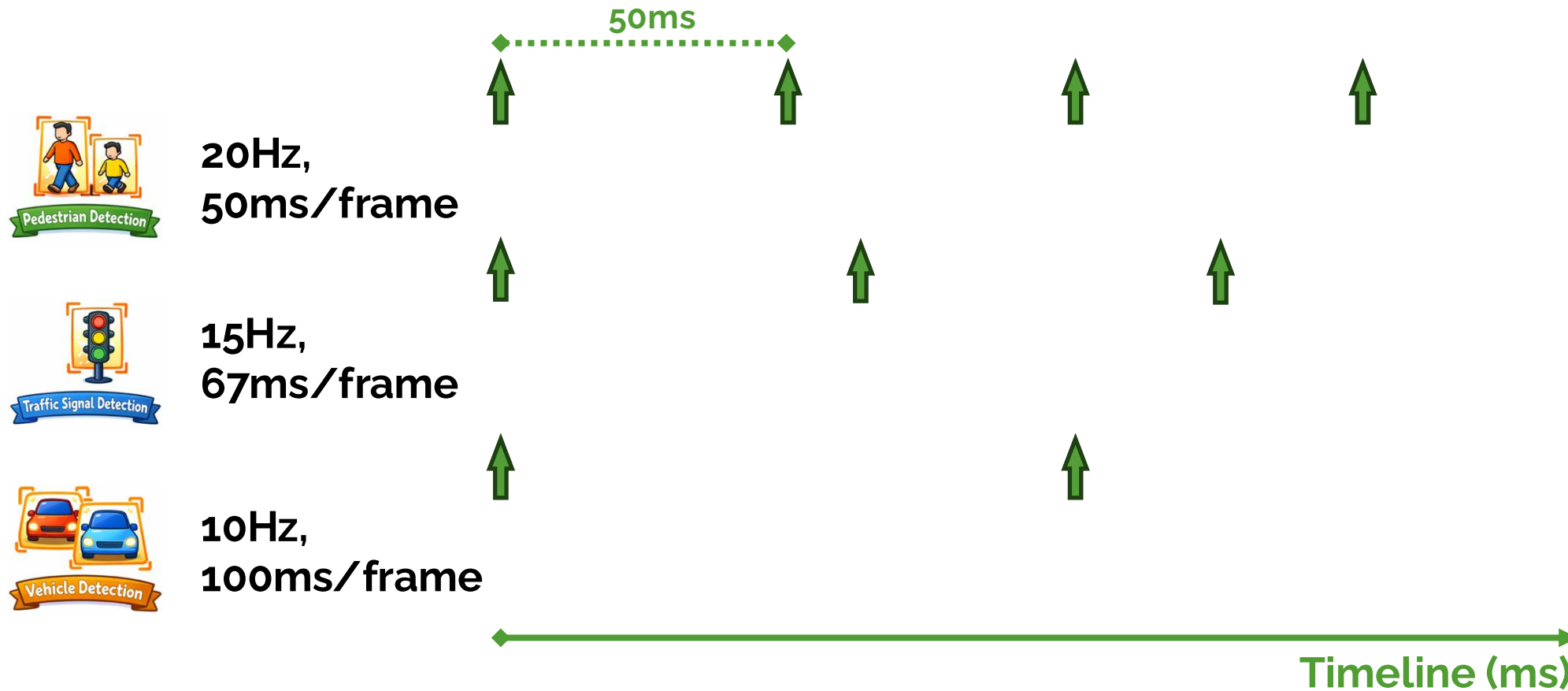
15Hz,
67ms/frame



10Hz,
100ms/frame

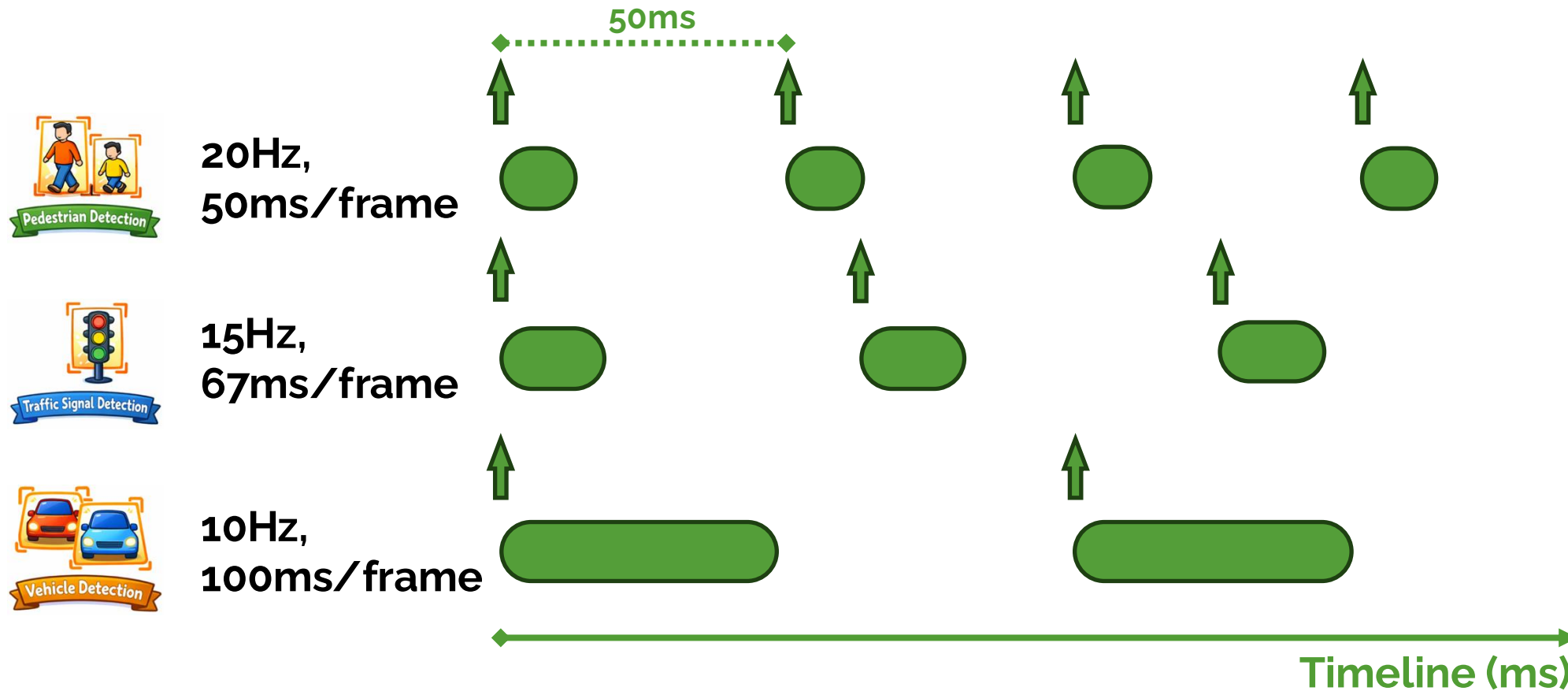
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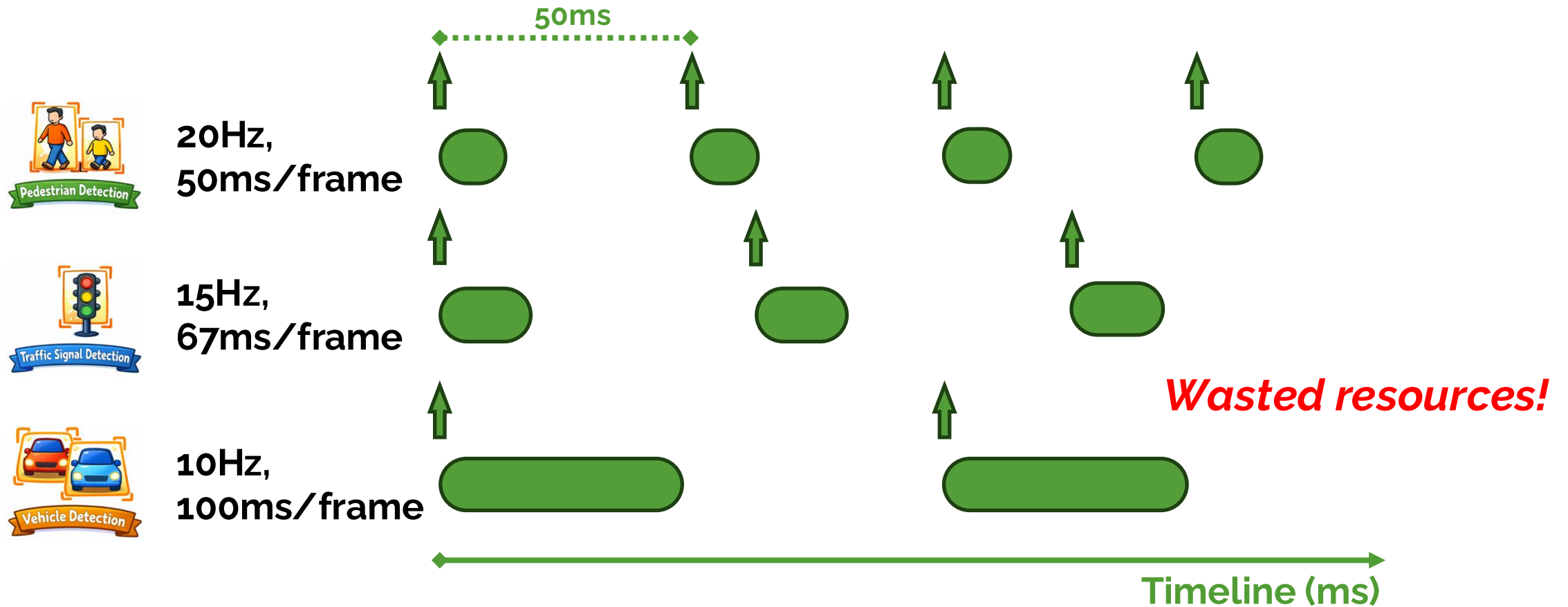
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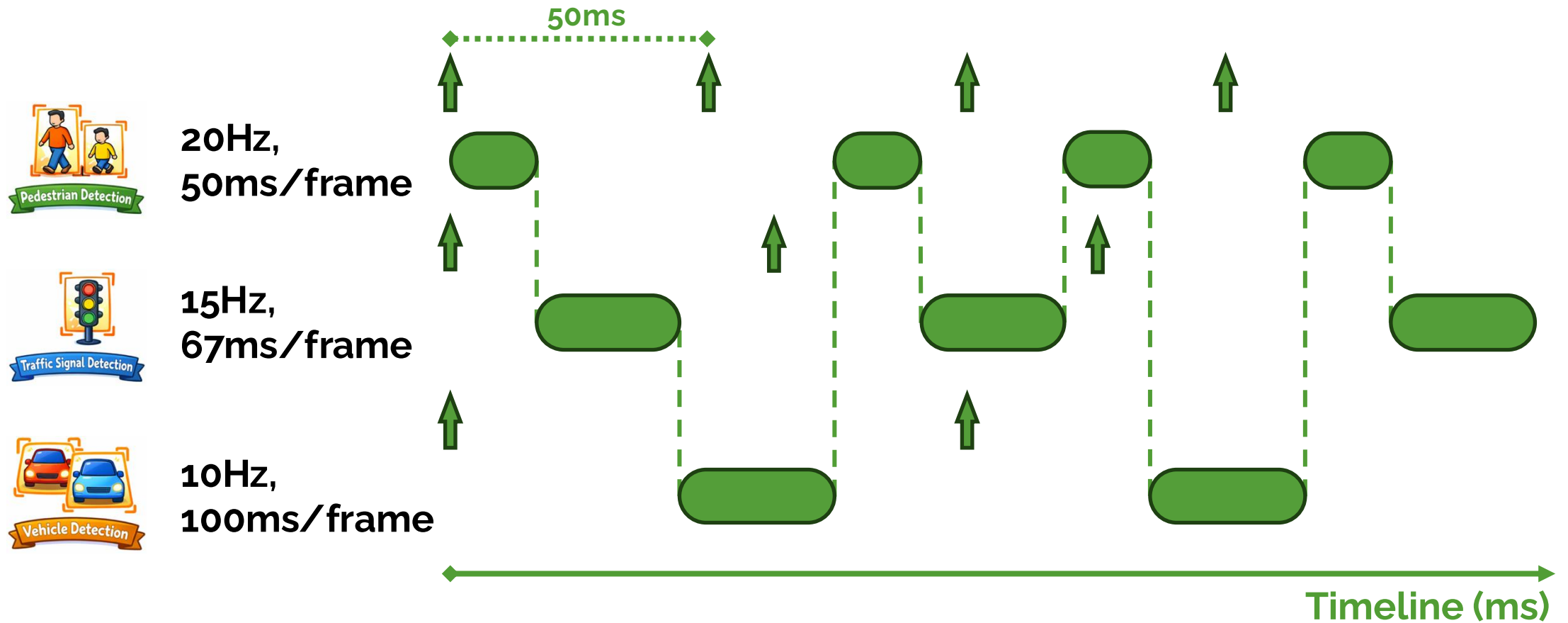
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Challenges

Solutions

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Challenges

- The system requires periodic execution of multiple tasks.
- Diverse scenarios, complicated hardware, and flexible objectives.
- Technique improvements may flip the design choices.

Solutions

- **Integrate real-time systems into modeling.**
- **Build an analysis tool that takes safety and resources as constraints, and determines the optimal choices.**
- **Explore improvements in terms of key features.**

Experimental Setup

Experimental Setup

- **ADS Setup:** Periodic tasks with hard deadline constraints.



Periodic tasks models

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- **Applications Setup:** DeiT^[1], MLP-Mixer^[2], PointNet^[3].



Periodic tasks models

	DeiT-S	DeiT-L	MLP-Mixer-S	MLP-Mixer-L	PointNet-S	PointNet-L
Setup A		20Hz		15Hz		10Hz
Setup B	20Hz			15Hz		10Hz
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Setup D	20Hz		15Hz		10Hz	

Representative applications

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State-of-the-art Accs.

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- **Evaluation Metric:** Busy ratio.



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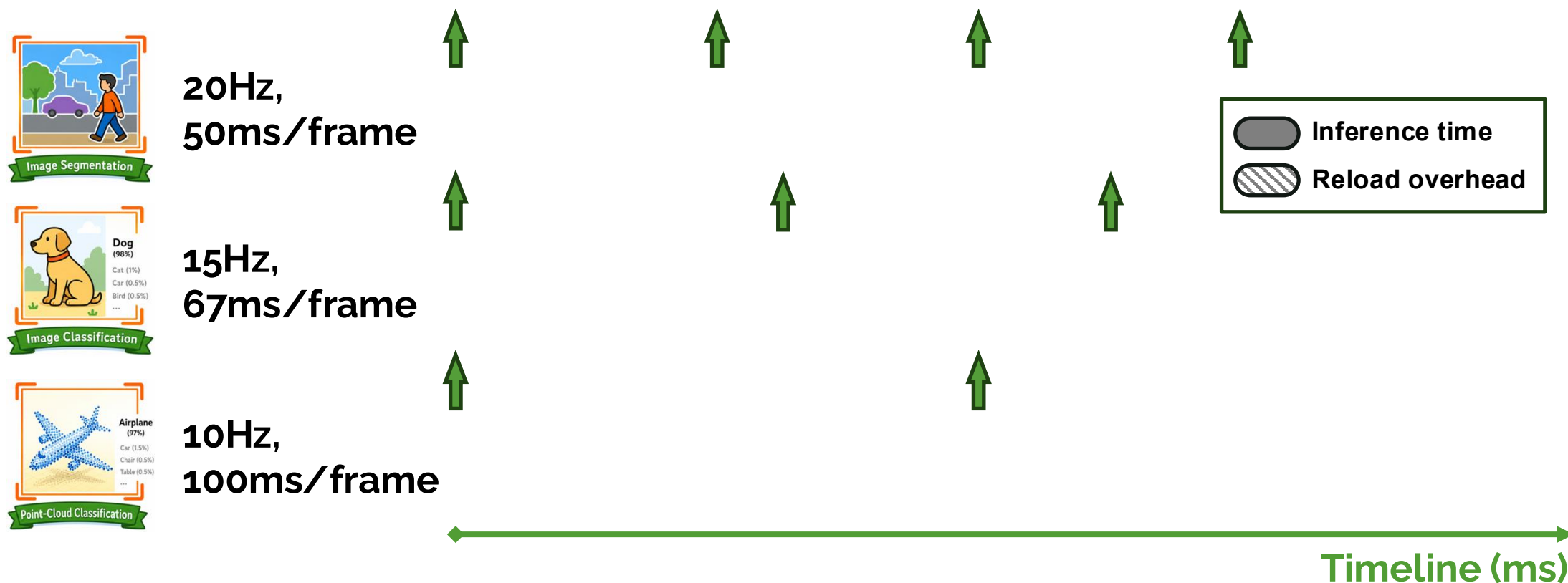
Trade-off with state-of-the-art techniques

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- Instantiate **dedicated acc** and reload bitstream **with 20ms overhead**.

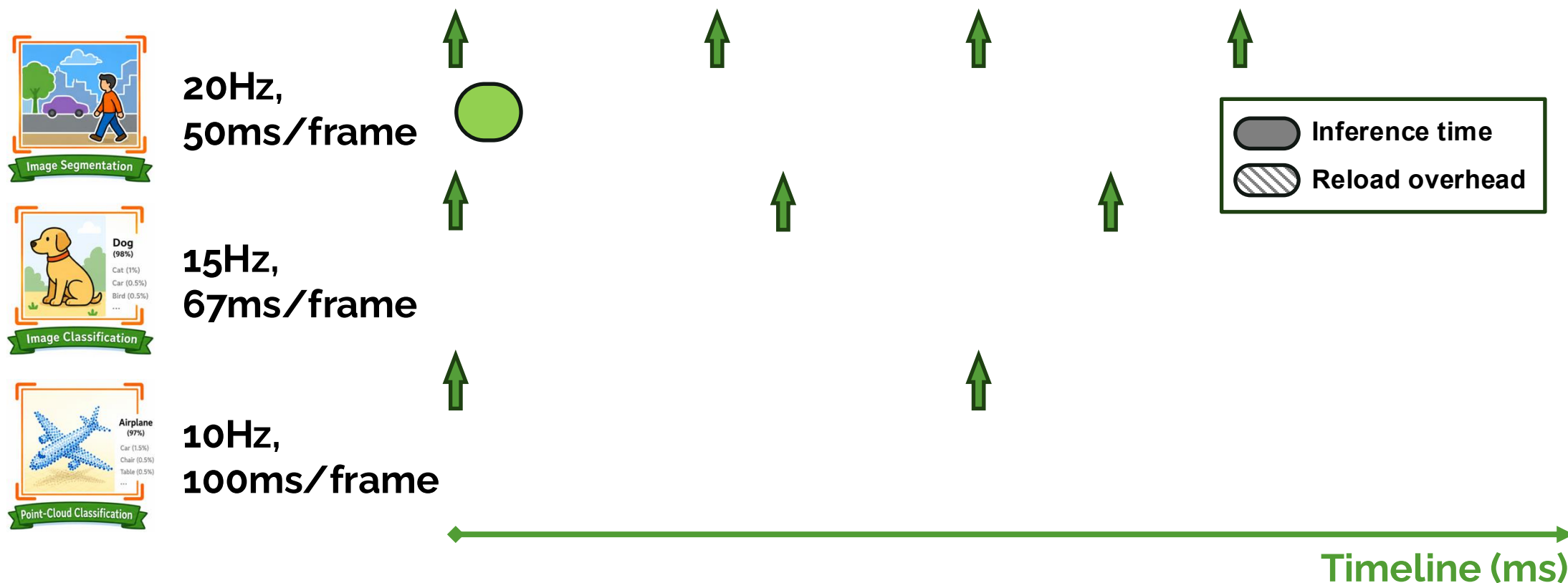
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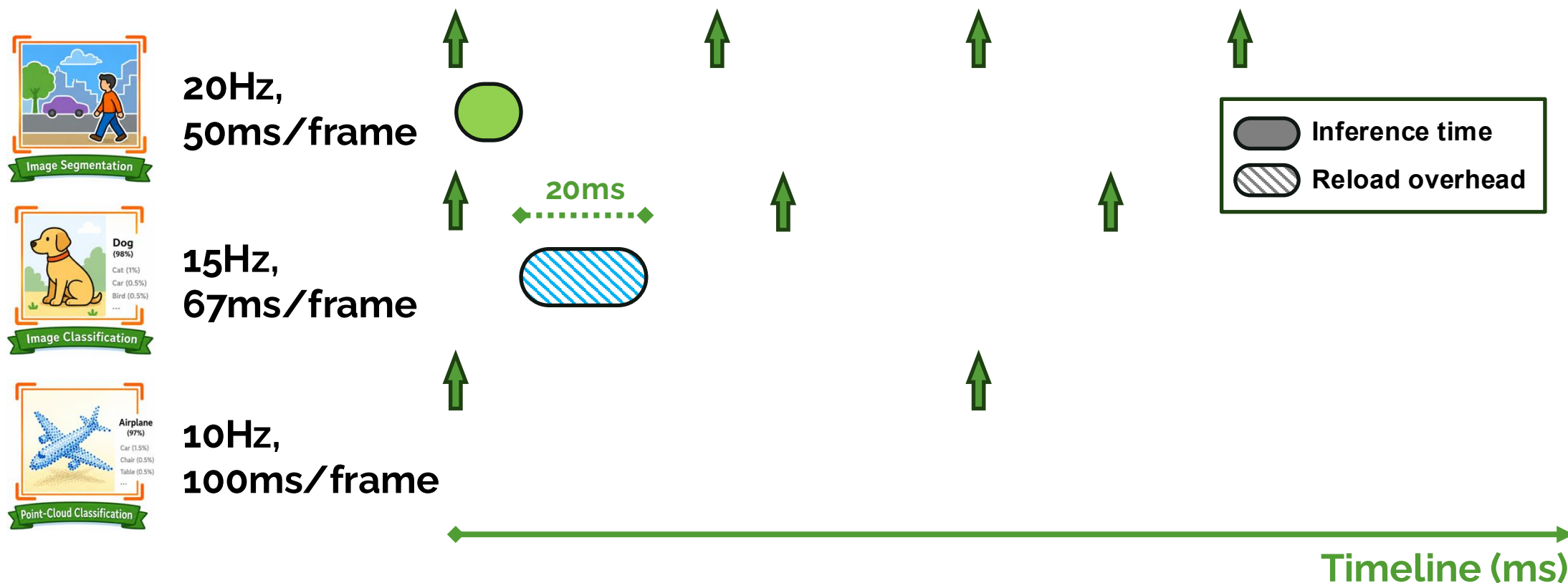
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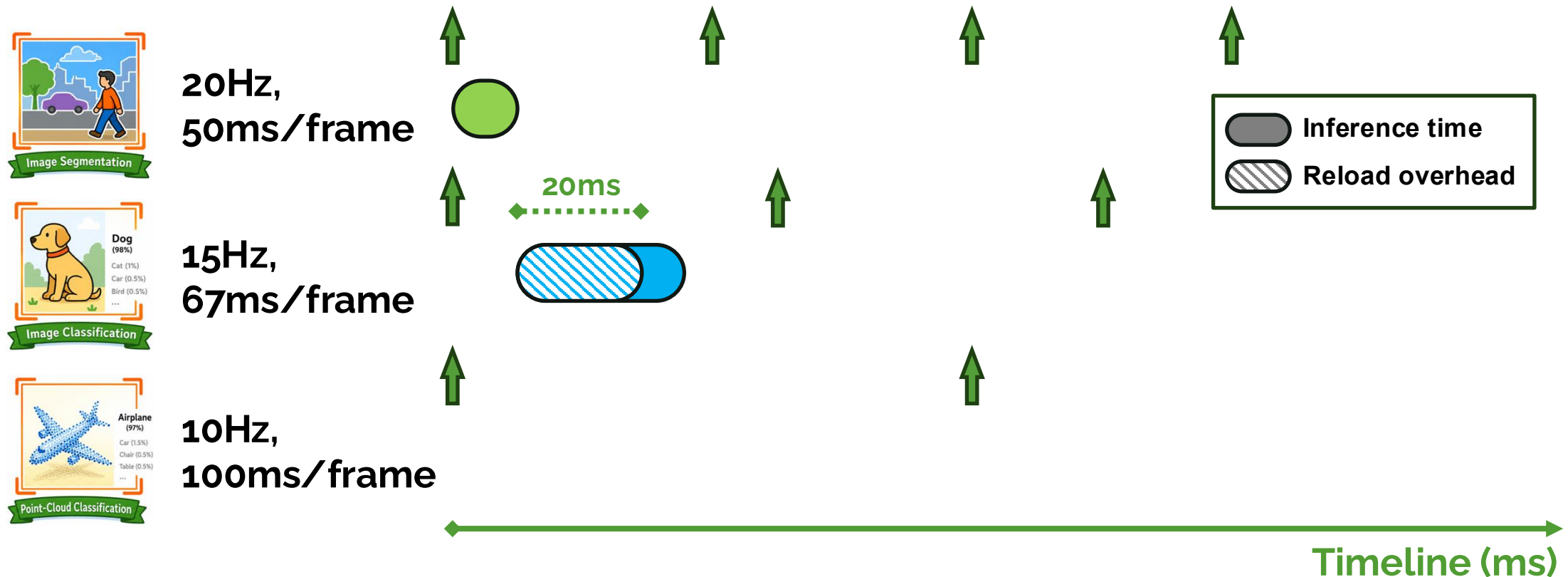
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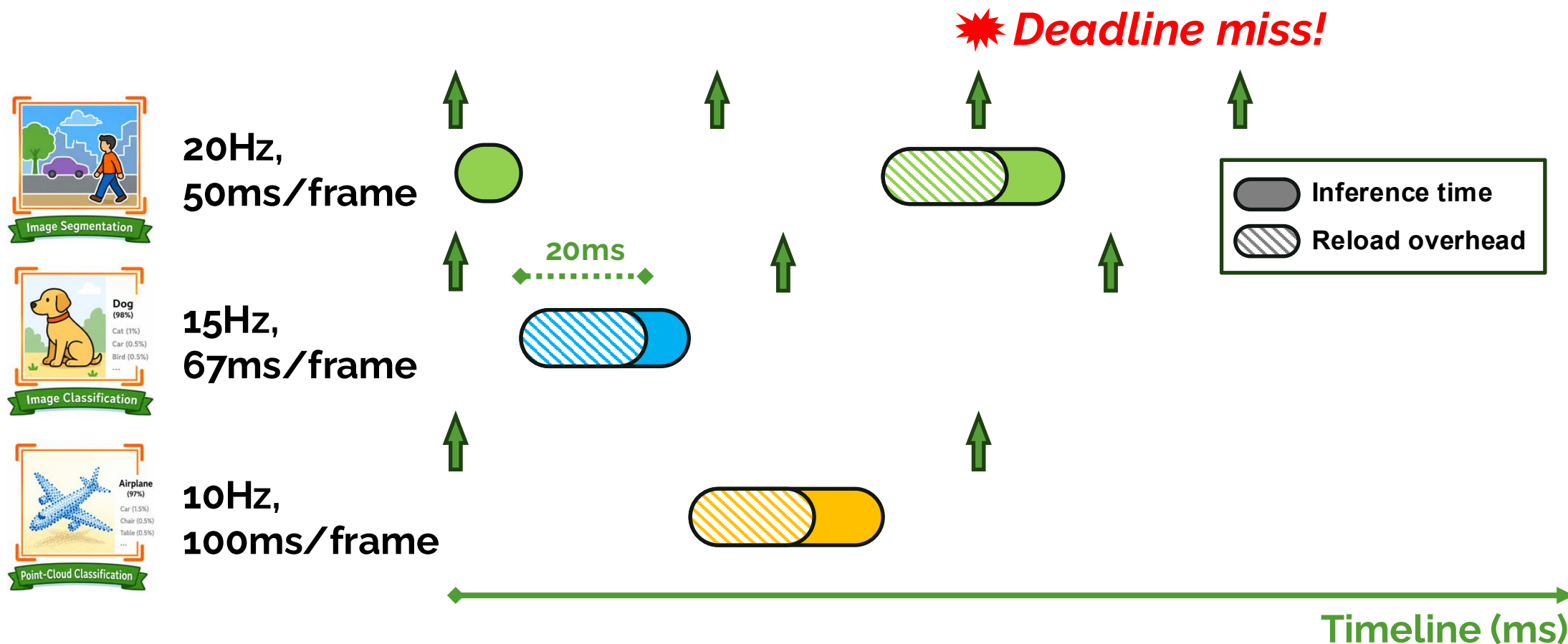
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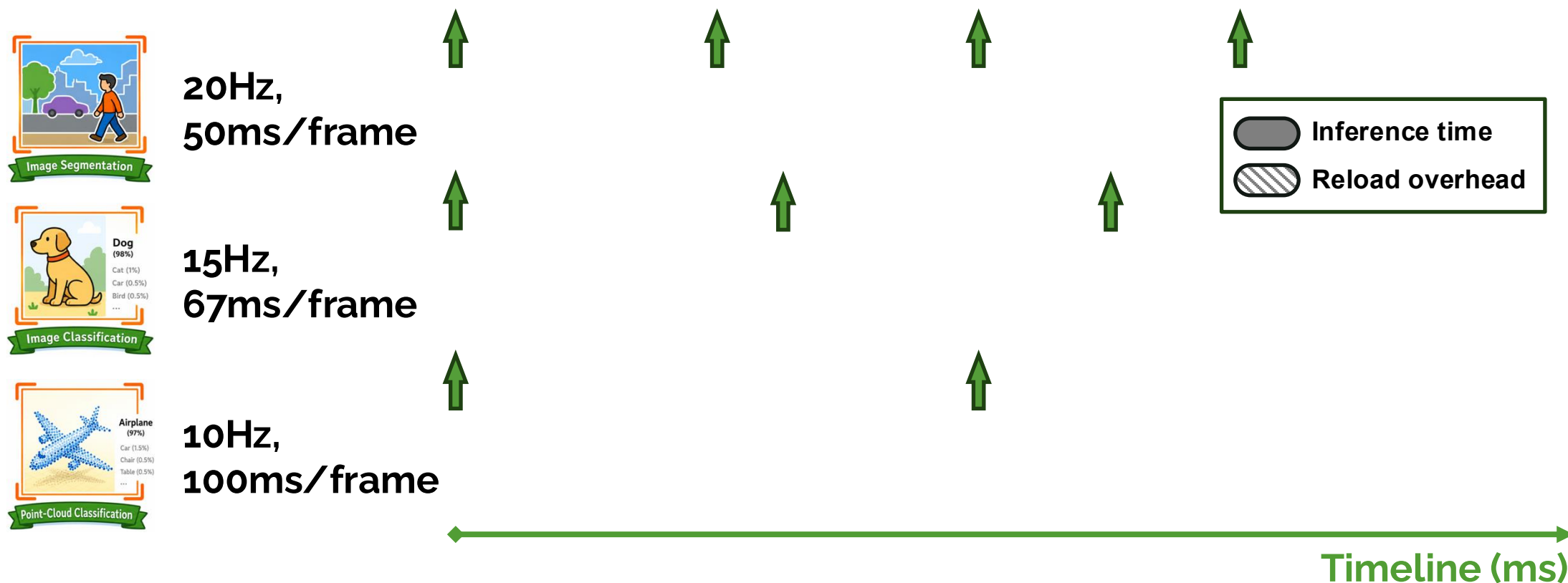
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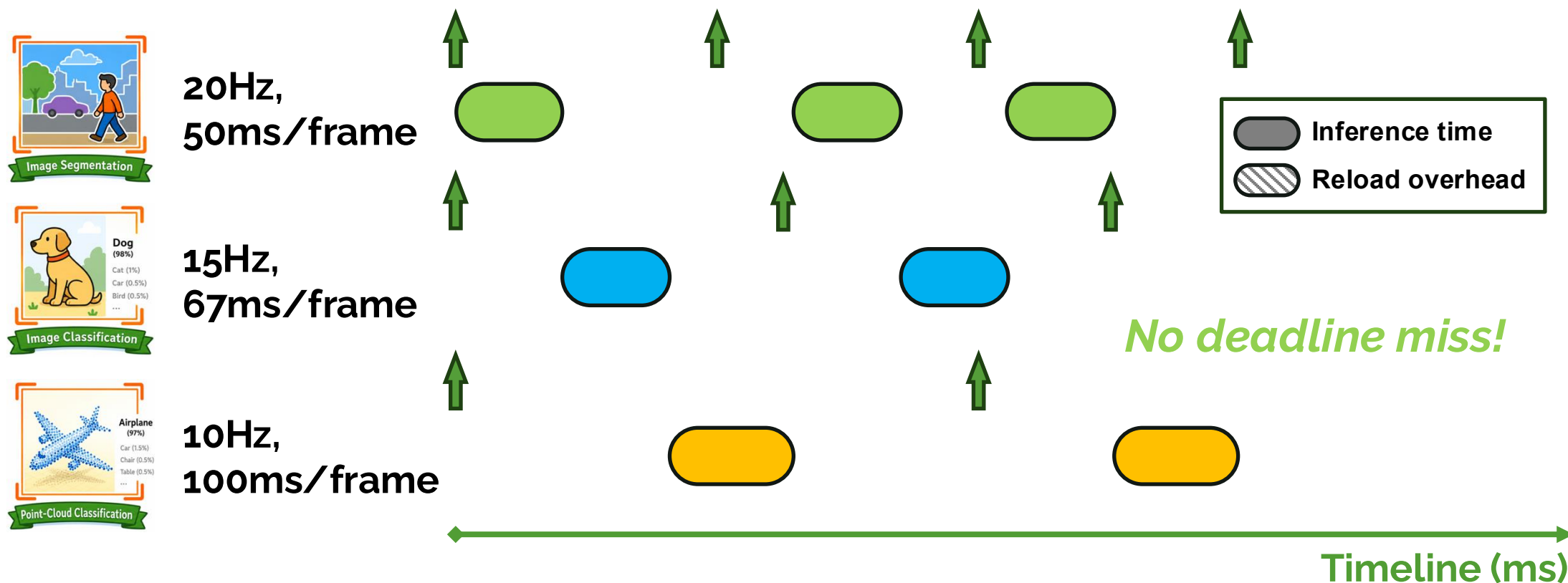
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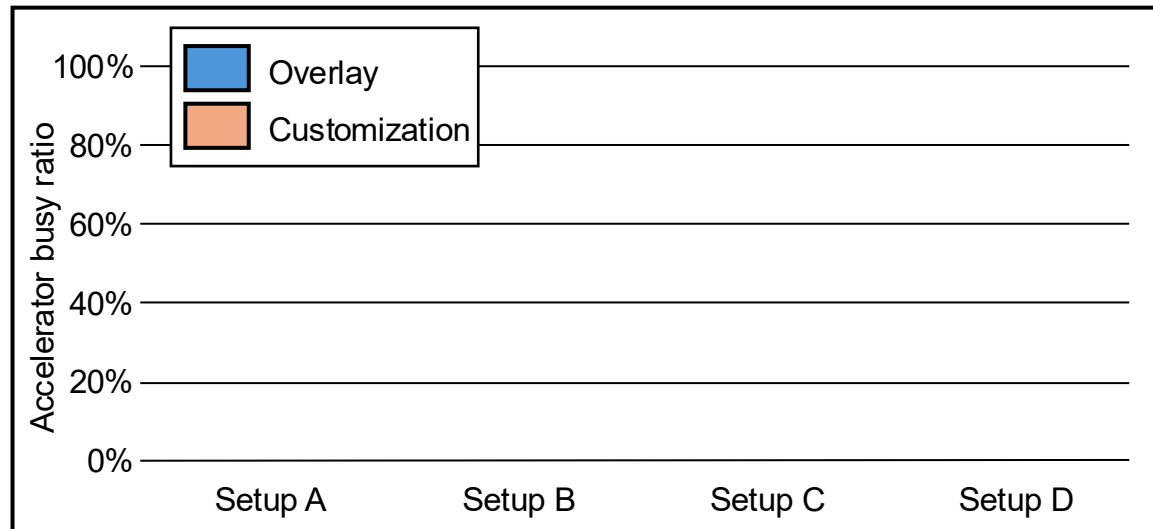
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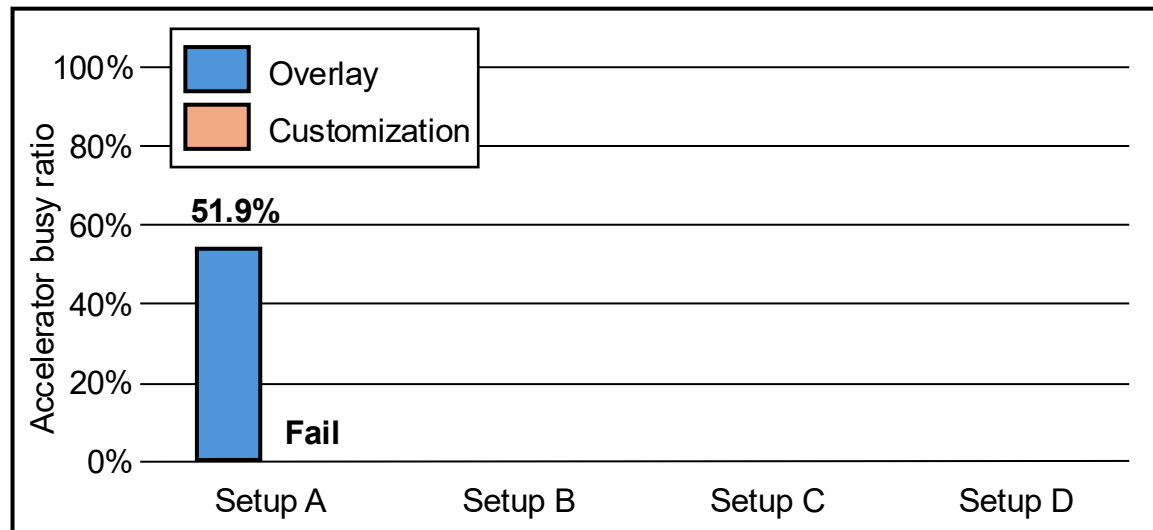
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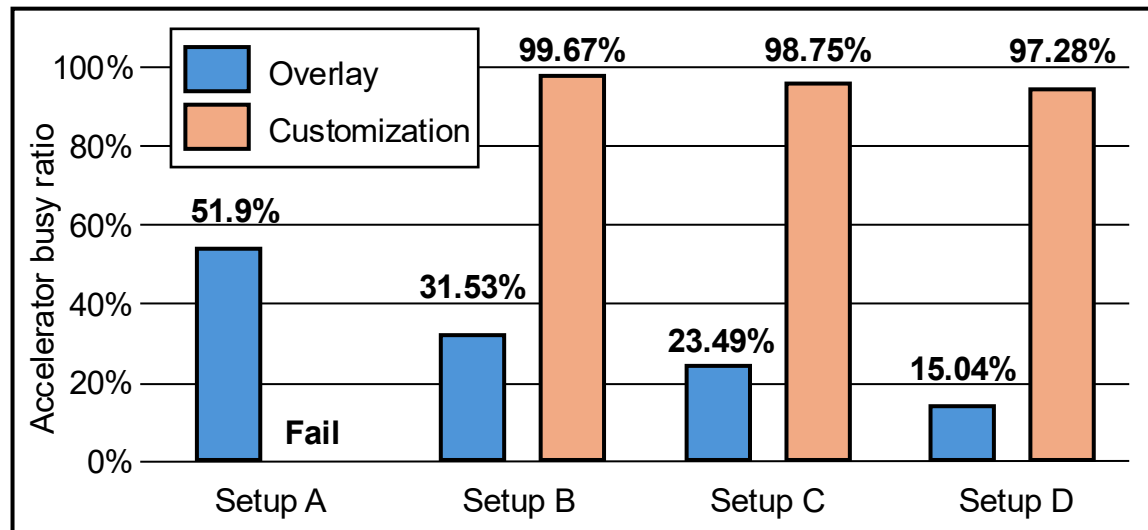
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- Setup A: Customization choice fails with **deadline miss**.



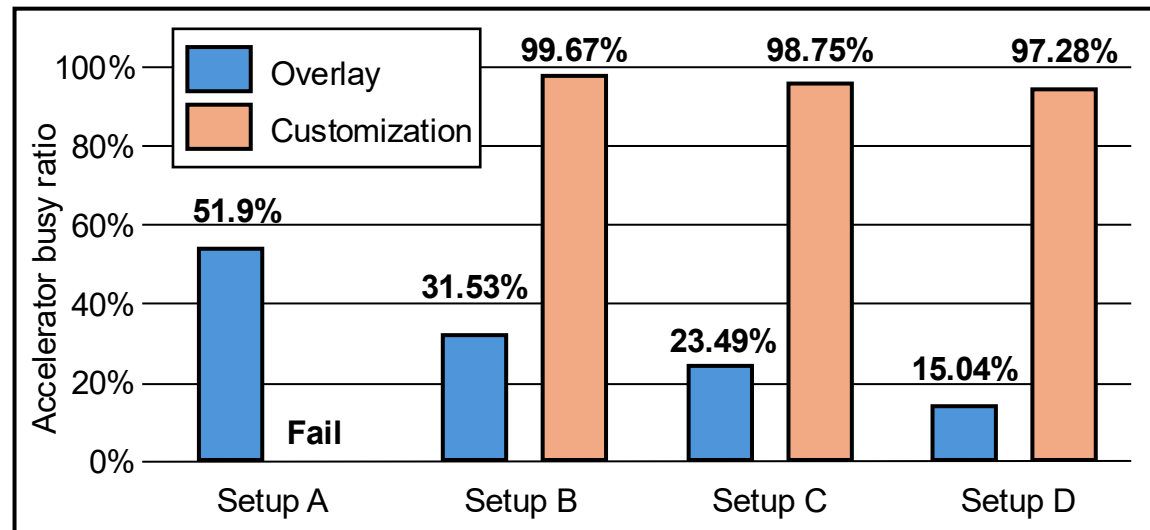
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Trade-off with state-of-the-art techniques

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- Under current techniques, **overlay** usually outperforms customization in **frequent switch scenarios**.

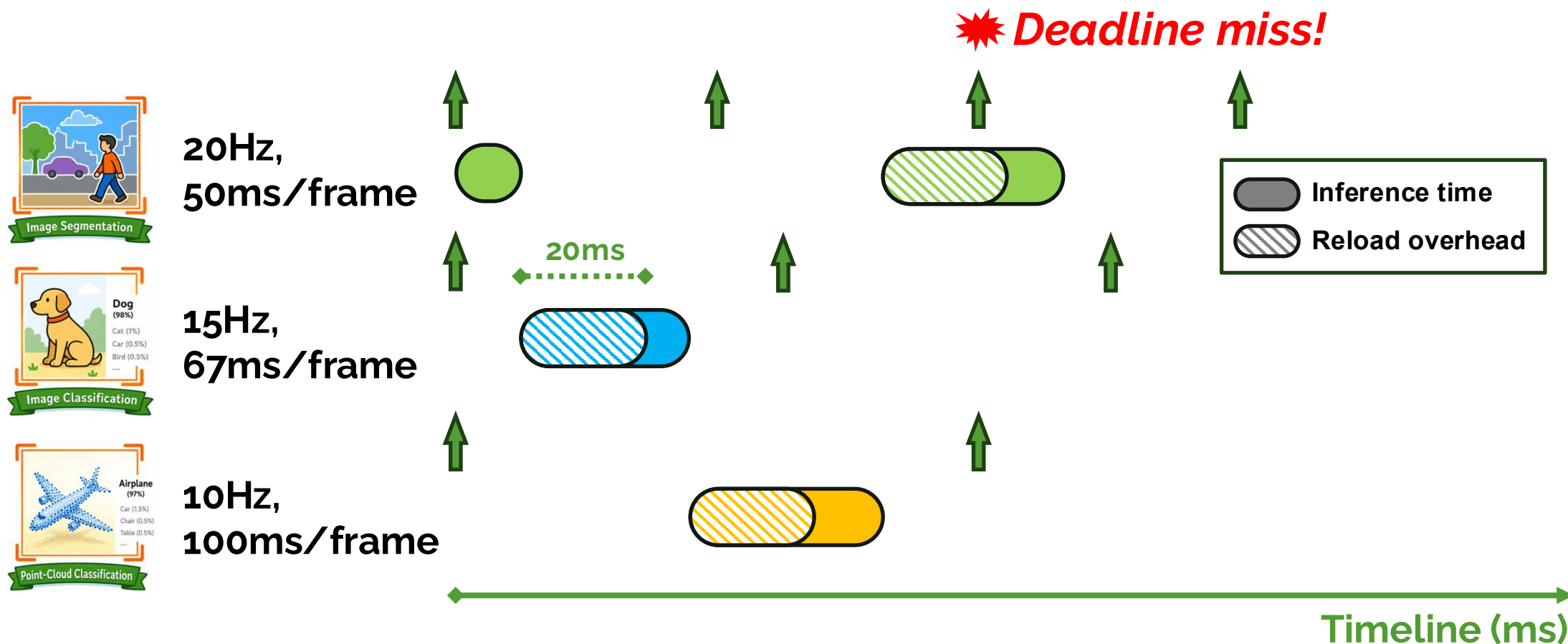
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- Reconfiguration latency can be **reduced** in the future.

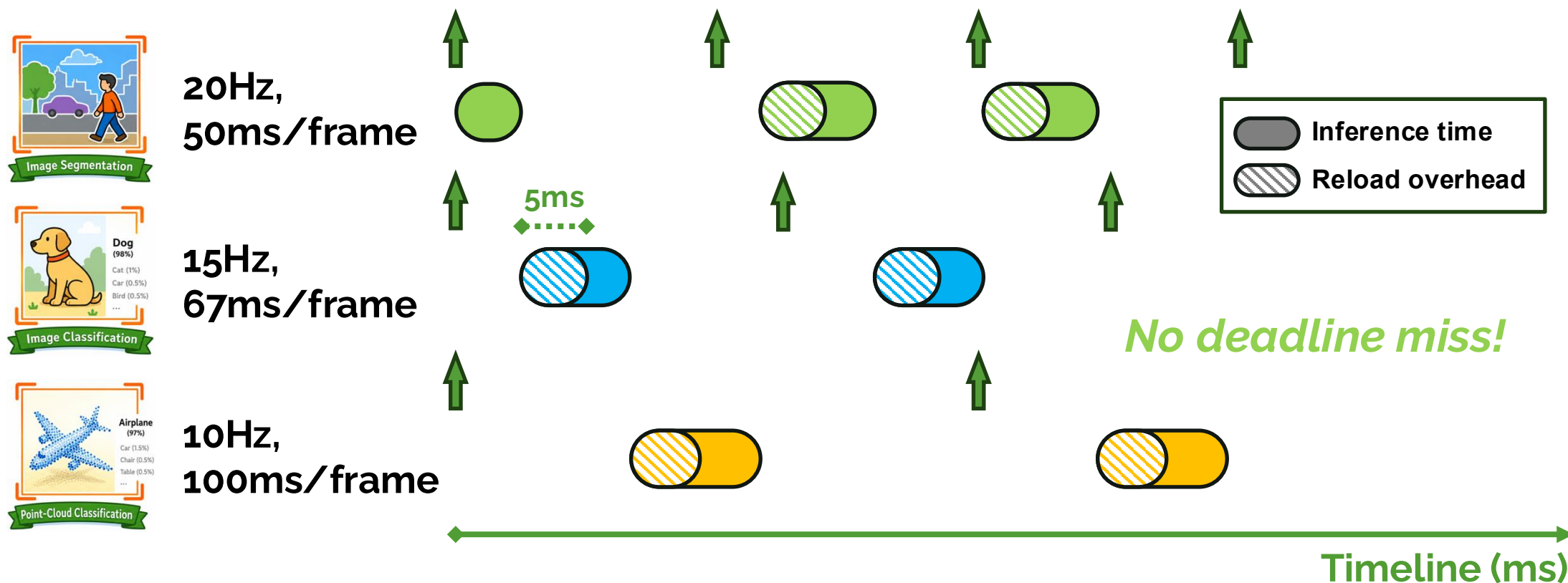
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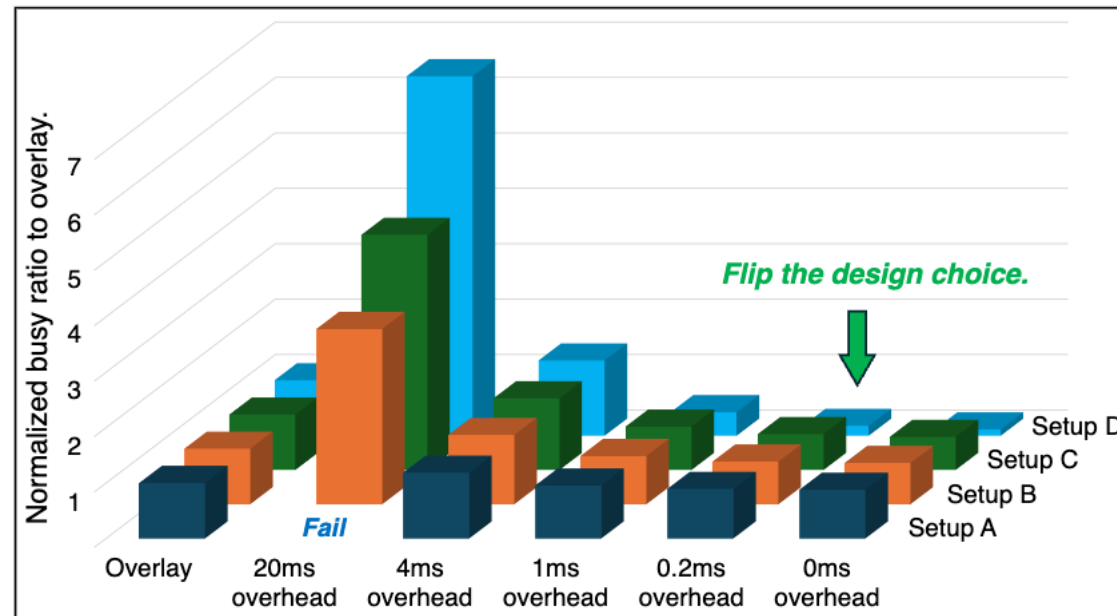
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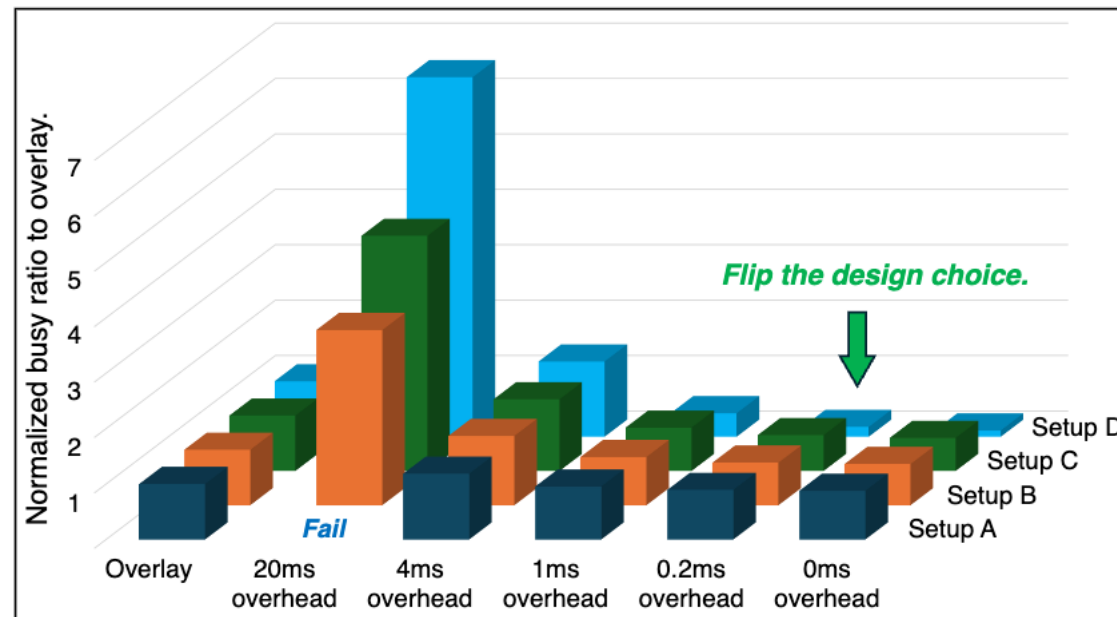
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Comparison under advanced reconfiguration techniques.

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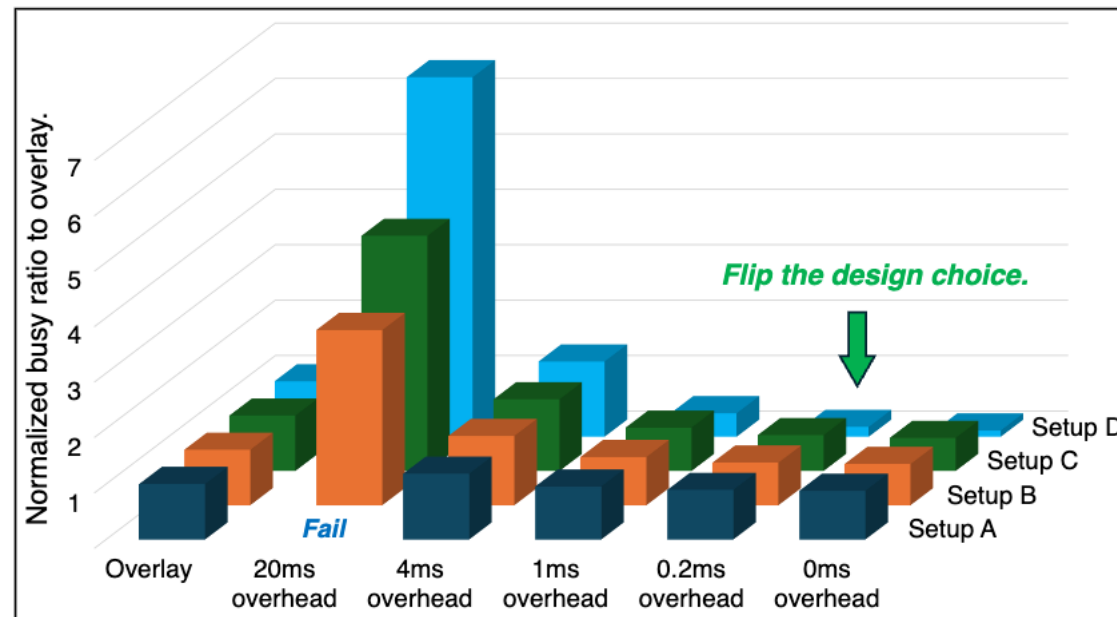
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- **0.2ms** reconfiguration latency will **flip the design choices**.



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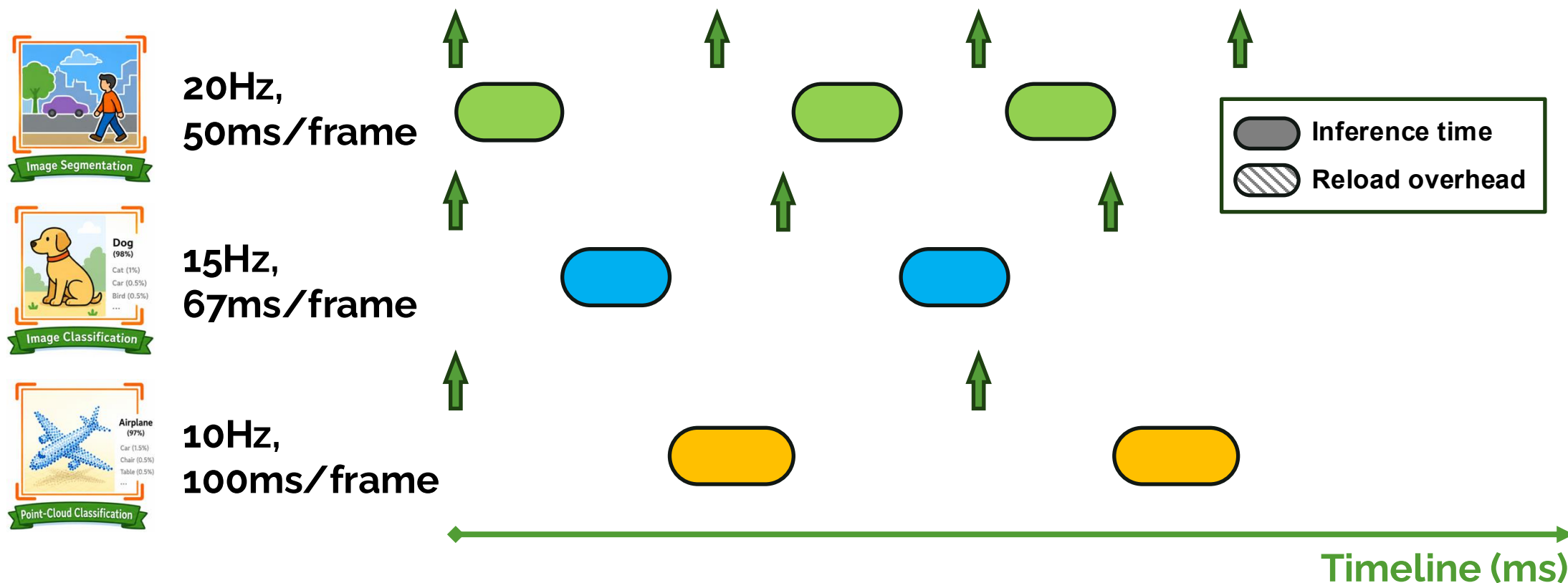
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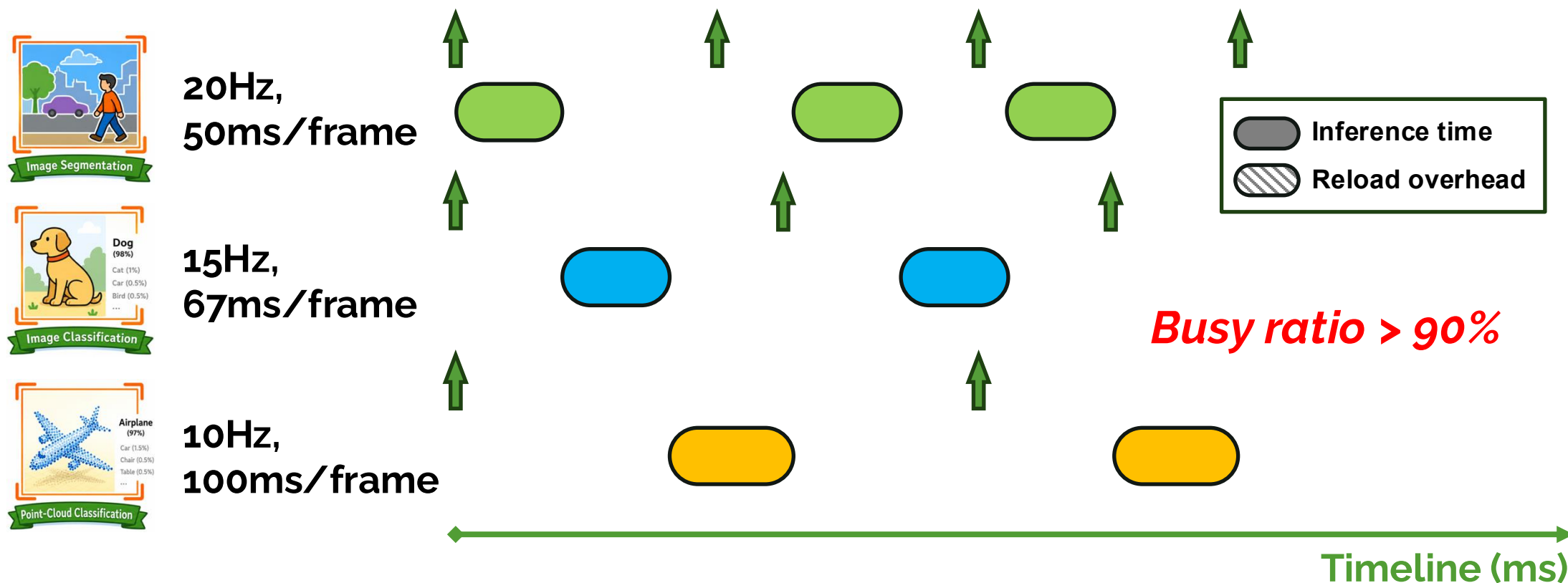
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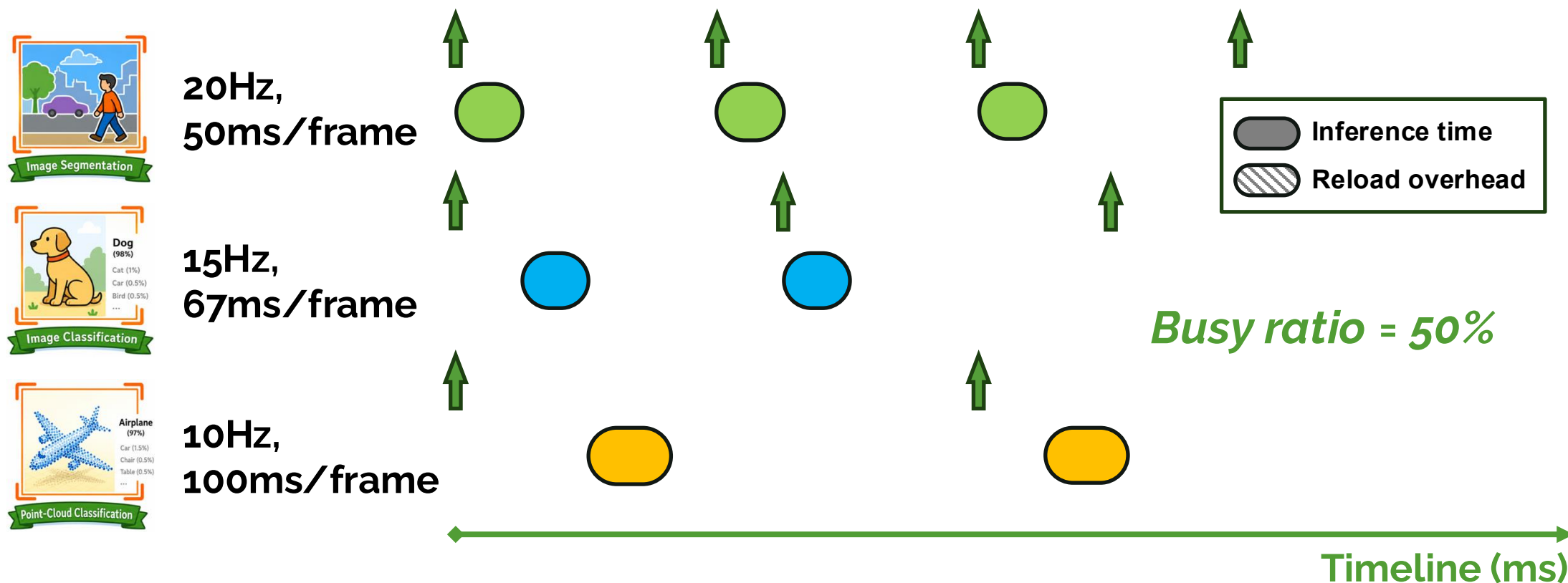
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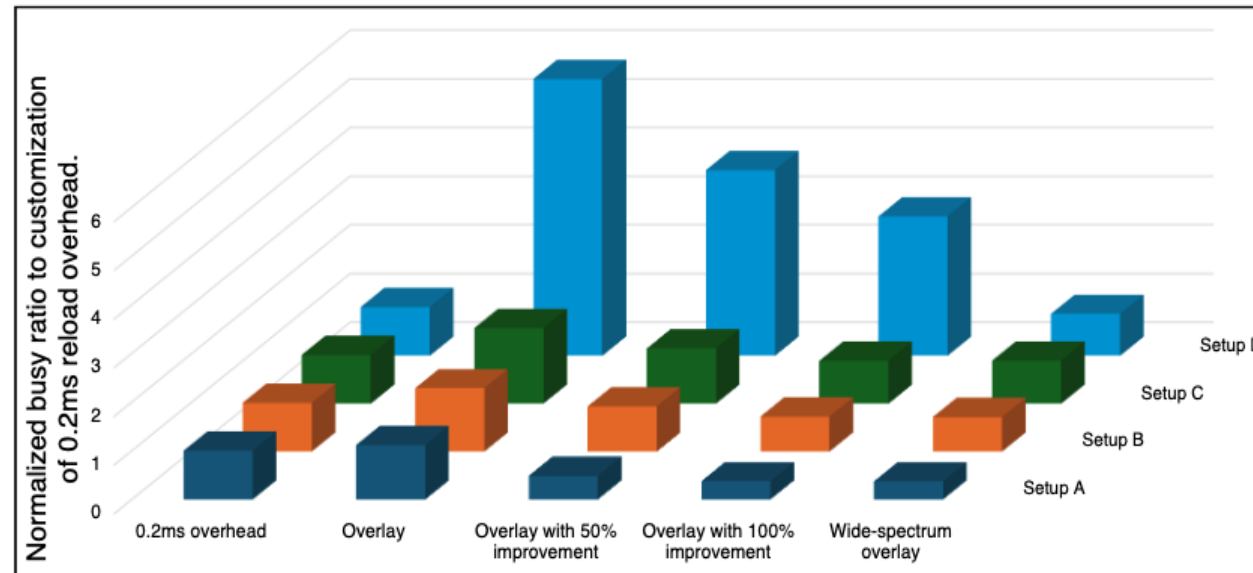
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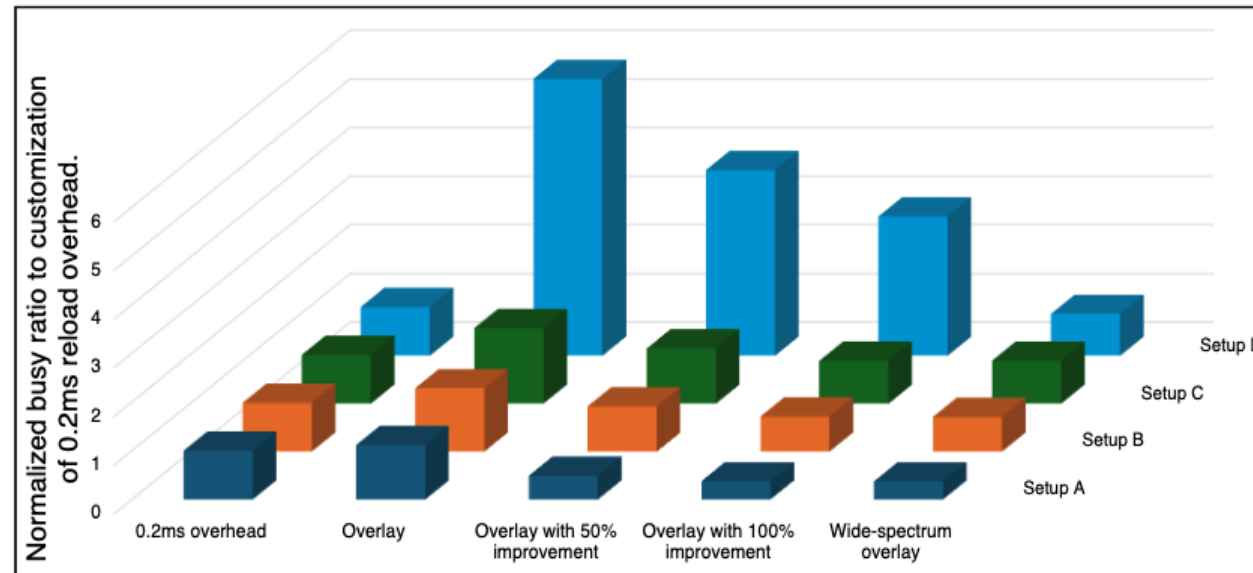
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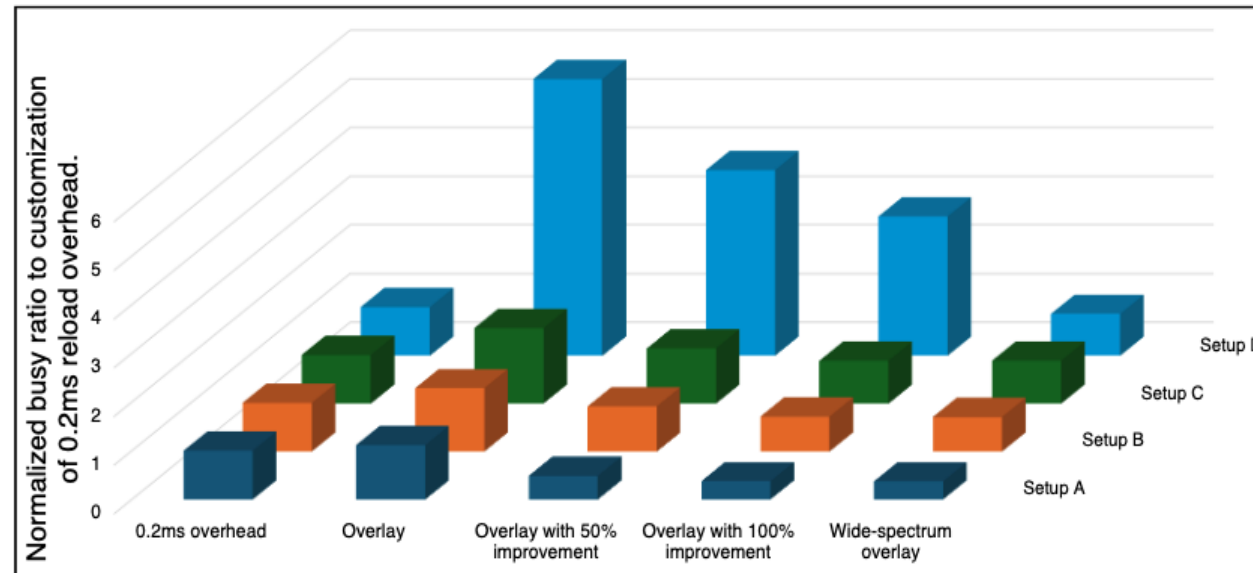
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Comparison under advanced overlay accelerators.

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- Current overlay acc struggles on **diverse workload** scenarios.
- **Wide-spectrum overlay** that covers entire range of workload diversity is necessary.



Comparison under advanced overlay accelerators.

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- Optimal architecture for one task is **not necessarily the best** for the whole ADS system.
- **Reconfiguration overhead** is a significant factor. Even a few milliseconds' reduction affects a lot.
- Customized architectures become attractive when bitstream reload overhead is **sufficiently reduced**.
- **Advanced overlays** can reverse the trend again.

Thank You & Welcome to questions!

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